

Abstract

A pulse width modulator is presented herein and it includes a pulse width driver circuit that receives a time varying ramp signal that is modulated by a time
5 varying analog input signal to produce a pulse width modulated drive signal comprised of turn-on pulses having widths that vary with variations in the magnitude of said input signal. A switch is turned on by each drive pulse for a time duration dependent upon
10 the duration of each drive pulse and provides an output signal therefrom. A negative bias amplifier provides a negative bias signal that has pulsations that are synchronized with said drive pulses but of opposite phase. A combiner combines output signal with the
15 negative bias signal to provide a combined output signal.